ITC-CSCC 2017

The 32nd International Technical Conference on Circuits/Systems, Computers and Communications

Spotlighting on Young Researchers

July 2-5, 2017 | Haeundae Grand Hotel, Busan, Republic of Korea

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Welcome to ITC-CSCC

The 32nd International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC 2017) will be held on July 2-5 at Haeundae Grand Hotel, Marine City, Busan, Republic of Korea.

Topics

The conference is open to researchers from all regions of the world. Participation from Asia Pacific region is particularly encouraged. Proposals for special sessions are welcome. Papers with original works in all aspects of Circuits/Systems, Computers and Communications are invited. Topics include, but not limited to, the followings:

- **Circuits & Systems**
  - Computer Aided Design
  - Intelligent Transportation Systems & Technology
  - RF Circuits
  - Modern Control
  - Neural Networks
  - VLSI Design
  - Sensors & Related Circuits
  - Power Electronics & Circuits
  - Analog Circuits
  - Linear / Nonlinear Systems
  - Medical Electronics & Circuits
  - Semiconductor Devices & Technology
  - Verification & Testing

- **Computers**
  - Artificial Intelligence
  - Biocomputing
  - Computer Systems & Applications
  - Multimedia Service & Technology
  - Object Extraction & Technology
  - Security
  - Watermarking
  - Image Processing
  - Internet Technology & Applications
  - Motion Analysis
  - Computer Vision
  - Face Detection & Recognition
  - Image Coding & Analysis

- **Communications**
  - Antenna & Wave Propagation
  - Audio / Speech Signal Processing
  - Circuits & Components for Communications
  - IP Networks & QoS
  - MIMO & Space-Time Codes
  - Multimedia Communications
  - Visual Communications
  - Network Management & Design
  - Optical Communications & Components
  - Radar / Remote Sensing
  - Communication Signal Processing
  - Ubiquitous Networks
  - UWB - Mobile & Wireless Communications
  - Future Internet Architectures

PROCEEDINGS

All registered participants are provided with conference proceedings. Authors of the accepted papers are encouraged to submit full-length manuscripts to IEIE JSTS (Journal of Semiconductor Technology and Science) or IEICE Transactions. Papers passed through the standard editing procedures of the IEIE JSTS or IEICE Transactions will be published in regular issues. The authors (or their institute) are requested to pay the publication charge for the IEIE JSTS or IEICE Transactions when their paper is accepted.

SUBMISSION OF PAPERS

Prospective authors are invited to submit original papers (1~4 pages) of either MS Word or PDF format written in English. Paper submission procedures are availabe at www.itc-cscc2017.org

AUTHOR'S SCHEDULE

- Submission of Paper : ~~March 20, 2017~~ **March 31, 2017**   • Notification of Acceptance : April 24, 2017   • Submission of Final Paper : May 15, 2017

Hosted by

The Institute of Electronics and Information Engineers (IEIE), Korea
The Institute of Electronics, Information and Communication Engineers (IEICE), Japan
The Electrical Engineering/Electronics, Computer, Telecommunications and Information Association, Thailand

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